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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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2SK2408

Silicon N-Channel MOS FET



ADE-208-1358 (Z) 1st. Edition Mar. 2001

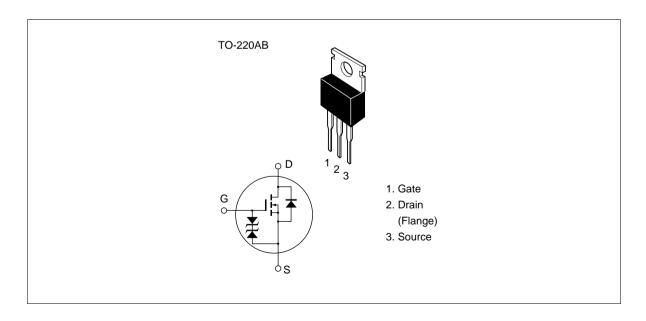
Application

High speed power switching

Features

- Low on-resistance
- Built-in fast recovery diode ($t_{rr} = 120 \text{ ns typ}$)
- High speed switching
- Low drive current
- Suitable for switching regulator, Motor control

Outline



2SK2408

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	500	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I _D	7	А
Drain peak current	l *1 D(pulse)	28	А
Body to drain diode reverse drain current	I _{DR}	7	Α
Channel dissipation	Pch*2	60	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. Value at Tc = 25 °C

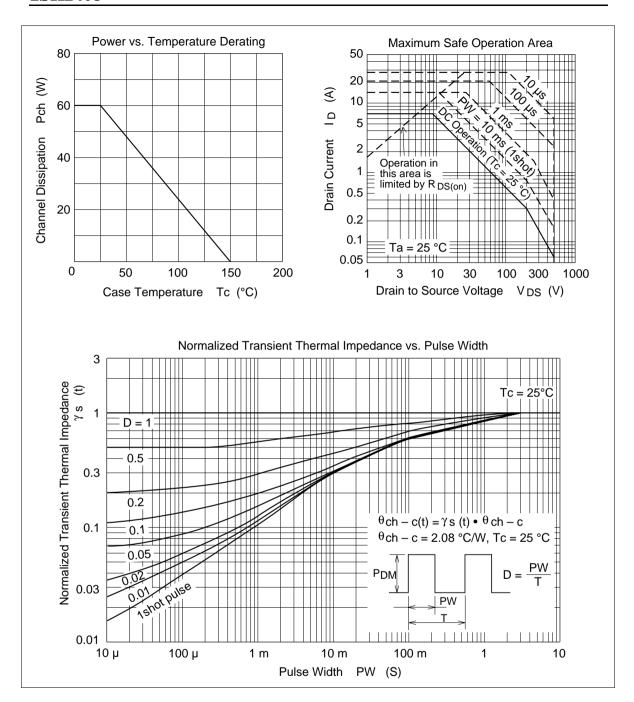
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	250	μA	$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	_	0.7	0.9	Ω	$I_D = 4A$ $V_{GS} = 10 V^{*1}$
Forward transfer admittance	y _{fs}	3.5	6.0	_	S	$I_D = 4 A$ $V_{DS} = 10 V^{*1}$
Input capacitance	Ciss	_	1100	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	310	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	50	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	15	_	ns	I _D = 4 A
Rise time	t _r	_	55	_	ns	V _{GS} = 10 V
Turn-off delay time	$t_{\text{d(off)}}$	_	100	_	ns	$R_L = 7.5 \Omega$
Fall time	t _f	_	48	_	ns	
Body to drain diode forward voltage	V_{DF}	_	0.9	_	V	$I_F = 7 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	120	_	ns	$I_F = 7 \text{ A}, V_{GS} = 0,$ $di_F / dt = 100 \text{ A} / \mu \text{s}$

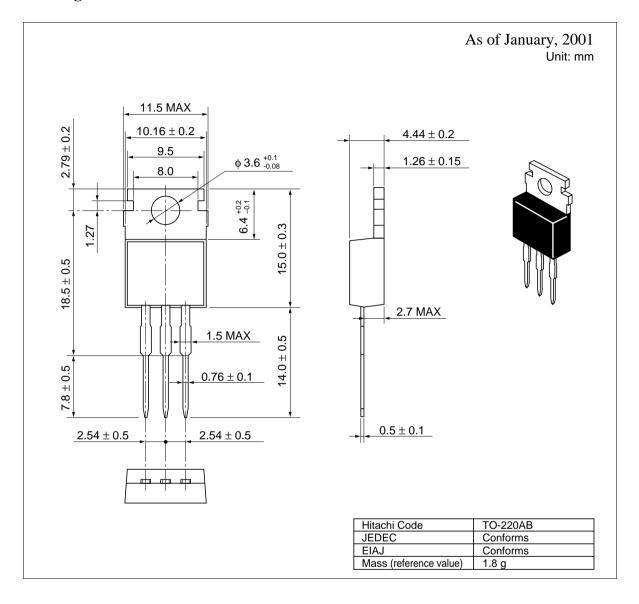
Note 1. Pulse Test

See characteristic curves of 2SK1516

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Package Dimensions



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